

1. General Description

The ARF2001 is a low noise amplifier operating at 10MHz-5000MHz, power supply +5V operation, at 68mA working current, can provide 22dB gain, OP1dB 22dBm, Noise figure typical value of 0.7dB. The ARF2001 port impedance is 50 Ω.

2. Features

- 8 Pin 2X2 mm DFN Package
- 0.01GHz to 5GHz operation
- NF ≤ 1.2dB across 0.01GHz to 5GHz
- >19dB gain across 0.01GHz to 4.2GHz
- >35.5dBm OIP3 @ 6mA IDD, 2.6GHz
- Shut-down mode pin with 1.8V TTL logic
- Supply voltage: +3V to +5V ; does not require -Vgg
- Maintains OFF state with high input power drive
- Unconditionally stable

3. Applications

- LTE / WCDMA / CDMA / GSM / Massive MIMO
- Repeaters / DAS
- TDD or FDD systems
- General Purpose Wireless

4. Functional Block Diagram

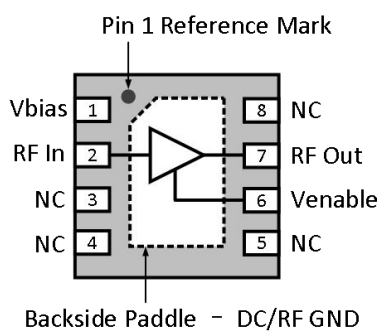


Figure1.

5. Order product model

ARF2001

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6. Specifications

6.1. Electrical Specifications

Test Conditions: 50Ω system, VDD=5V and VDD=3.3V, Temp=+25°C, (de-embedded data);

Table1. Electrical Specifications

Parameter	Symbol	Test Conditions	Min	Typ	Max	Units
Frequency Range			0.01		5	GHz
Test Frequency				2.6		GHz
Input Return Loss	S11	VDD=5V	9	11		dB
		VDD=3.3V	9	10		dB
Output Return Loss	S22	VDD=5V	7	8.5		dB
		VDD=3.3V	9	10.5		dB
Gain	S21	VDD=5V	20	22	24	dB
		VDD=3.3V	21	23	25	dB
Reverse ISO		VDD=5V	27	29	35	dB
		VDD=3.3V	27	29	36	dB
Off State Gain		VDD=5V,Pin=20dBm	-	-21	-20	dB
		VDD=3.3V,Pin=20dBm	-	-21	-20	dB
OP1dB		VDD=5V	21	22	24	dBm
		VDD=3.3V	17	18	20	dBm
OIP3		VDD=5V,Pout=+5 dBm/tone Δf=1 MHz	33	35.5	45	dBm
		VDD=3.3V,Pout=+5 dBm/tone Δf=1 MHz	30	34	40	dBm
Noise Figure	NF	VDD=5V	0.3	0.7	1	dB
		VDD=3.3V	0.3	0.58	1	dB
Drain Current		VDD=5V,Venable=0V	50	68	86	mA
		VDD=3.3V,,Venable=0V	60	78	96	mA
Drain Current		VDD=5V,Venable=5V	1	2	2.5	mA
		VDD=3.3V,,Venable=5V	1	2	2.5	mA
Venable Current		VDD=5V,Venable=5V	300	500	700	uA
		VDD=3.3V,,Venable=5V	200	350	500	uA

6.2. Handling Ratings

Table2. Handling Ratings

Symbol	Parameter	Min	Typ	Max	Units
T _{STG}	Storage temperature range	-65		+150	°C
V _{ESD}	Human body model (HBM)		1000		V
	Charged device model (CDM)		1000		V

6.3. Timing Requirements

Table3. Timing Requirements

Parameter	Conditions	Min	Typ	Max	Units
Switching Time OFF	VDD=5V or VDD=3.3V	10	100	200	ns
Switching Time ON	VDD=5V or VDD=3.3V	10	300	500	ns

Table4. Absolute Maximum Ratings

Parameter	Min	Typ	Max	Units
DC Supply Voltage (VDD)	0		7	V
DC Control Voltage	0		7	V
RF Input Power (Pin), CW, 50ohms, T=25°C			30	dBm
RF Input Power (Pin), CW, Off State, T=25°C			30	dBm
Operating Temperature	-40		105	°C

7. Absolute Maximum Ratings

Stresses beyond those listed under Absolute Maximum Ratings may cause permanent damage to the device. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

8. Pin Assignments and Description

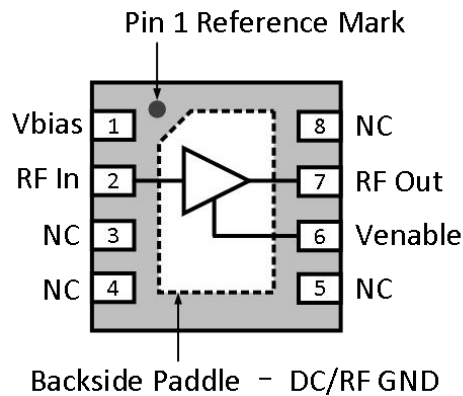


Figure2. Pin Assignments

Table5. Description

Pin No.	Mnemonic	Description
1	Vbias	Setting the Icq bias point for the device.
2	RFIN	RF Input pin, DC Block is required.
3, 4, 5, 8	NC	No electrical connection. Provide grounded land pads for PCB mounting integrity.
6	Venable	A high voltage ($\geq 1.1V$) turning off the device.
7	RFOUT	RF output pin.
Backside Paddle	DC/RF GND	Use recommended via pattern to minimize inductance and thermal resistance.

9. VDD=5V Typical Performance

Test Conditions: 50Ω system, VDD=5V, Temp=+25°C, (de-embedded data);

Table6. Typical Performance

Parameter	Conditions	Units	Typical					
Frequency		GHz	0.6	1.9	2.6	3.5	4.2	5
Input Return Loss		dB	11	12	11	11.5	11	9
Output Return Loss		dB	9	10	8.5	9	10.5	11
Gain		dB	25.2	23.6	22	20.2	19	17.5
OP1dB		dBm	21	22	22	22	21	20
OIP3	Pout=+5 dBm/tone Δf=1 MHz	dBm	35	35	35.5	36	35	35
Noise Figure		dB	0.5	0.6	0.7	0.85	0.9	1.2

10. VDD=5V Performance Plots

Test Conditions: 50Ω system, VDD=5V.

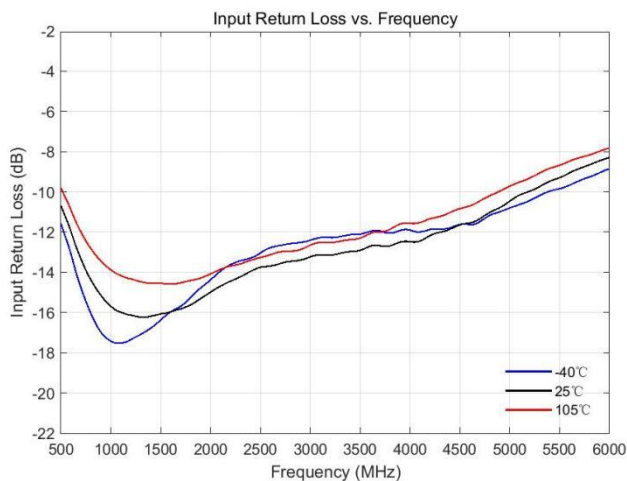


Figure3.

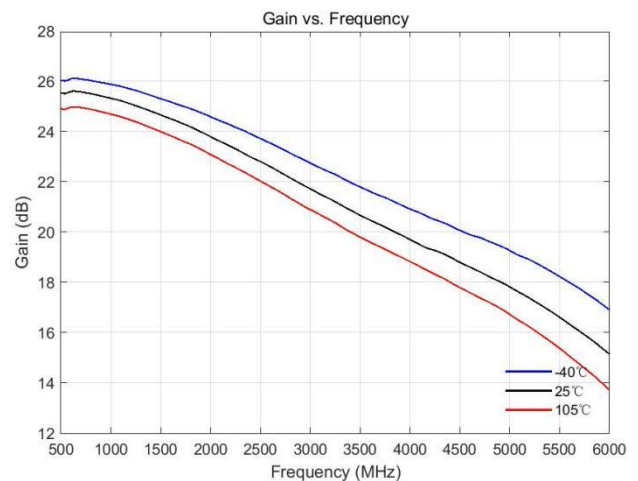


Figure4.

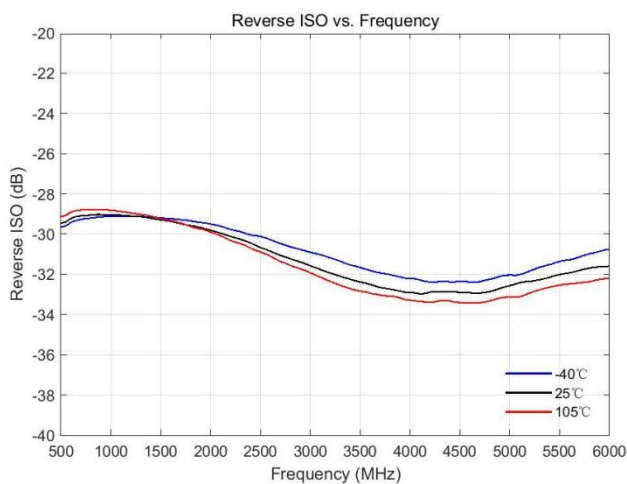


Figure5.

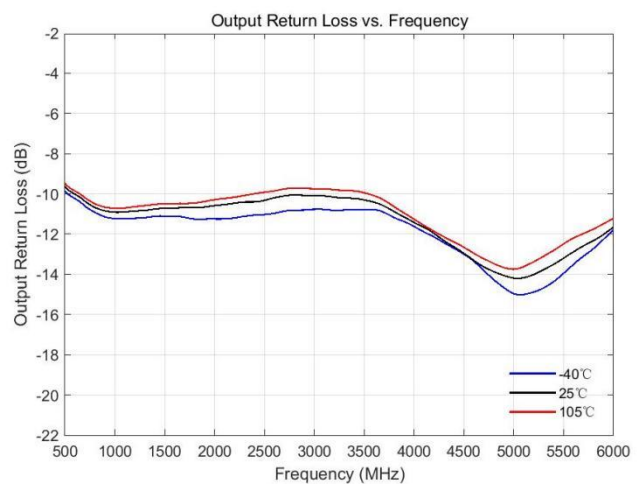


Figure6.

VDD=5V Performance Plots (Cont.)

Test Conditions: 50Ω system, VDD=5V.

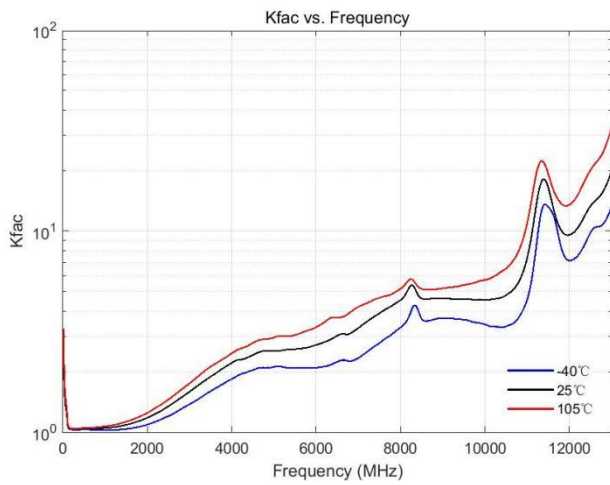


Figure7.

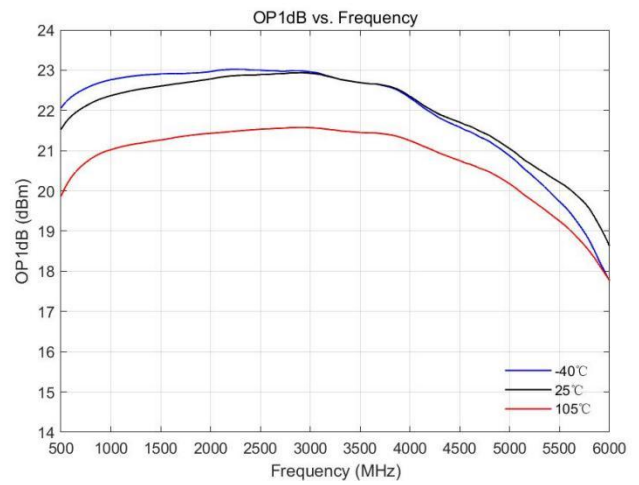


Figure8.

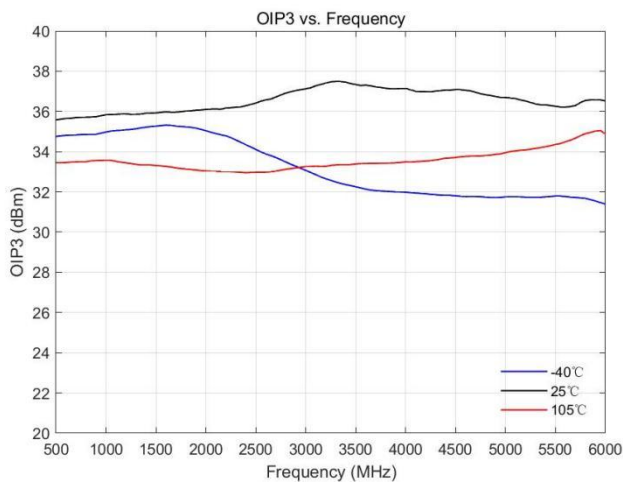


Figure9.

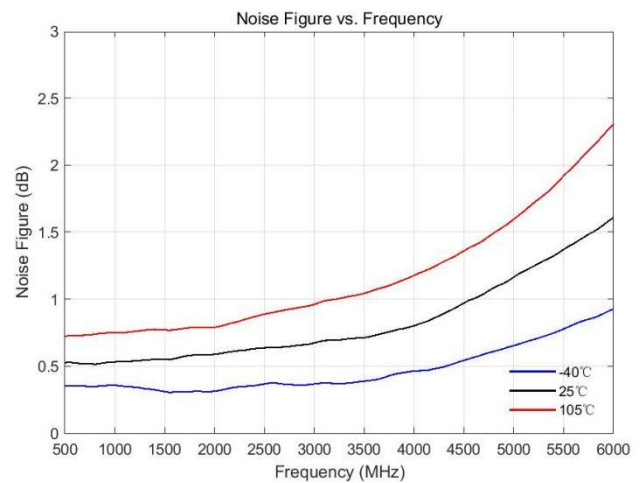


Figure10.

11. VDD=3.3V Typical Performance

Test Conditions: 50Ω system, VDD=3.3V, Temp=+25°C, (de-embedded data);

Table7. Typical Performance

Parameter	Conditions	Units	Typical					
Frequency		GHz	0.6	1.9	2.6	3.5	4.2	5
Input Return Loss		dB	9	11	10	10	10.5	9.5
Output Return Loss		dB	8	10	10.5	10.5	11	12
Gain		dB	25.5	24	23	21	20	19
OP1dB		dBm	18	18	18	17.5	17.0	16
OIP3	Pout=+5 dBm/tone Δf=1 MHz	dBm	33	34.5	34	35	34	31.5
Noise Figure		dB	0.5	0.55	0.58	0.70	0.75	1.0

12. VDD=3.3V Performance Plots

Test Conditions: 50Ω system, VDD=3.3V.

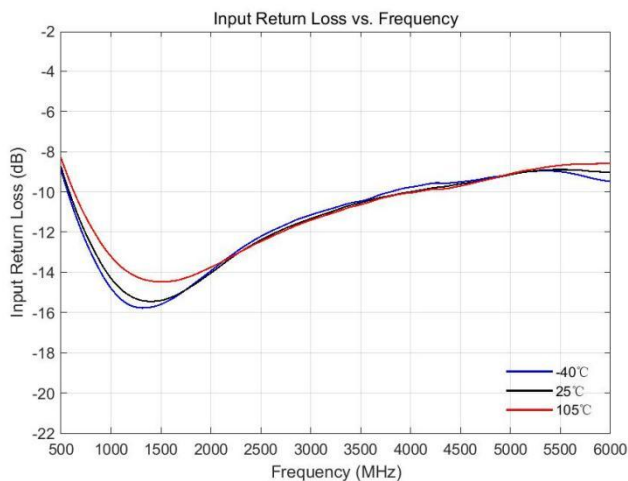


Figure11.

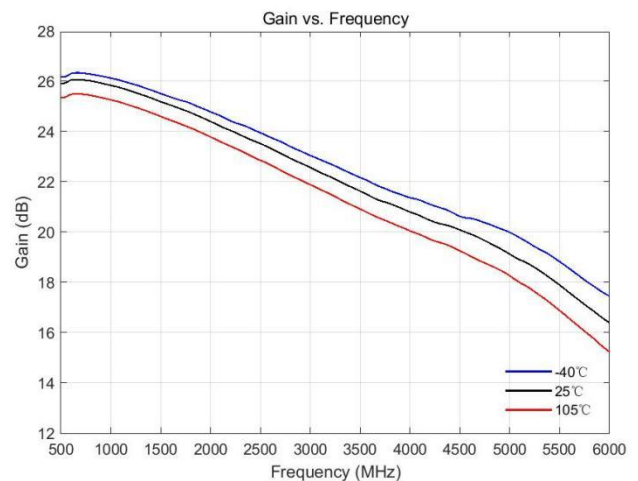


Figure12.

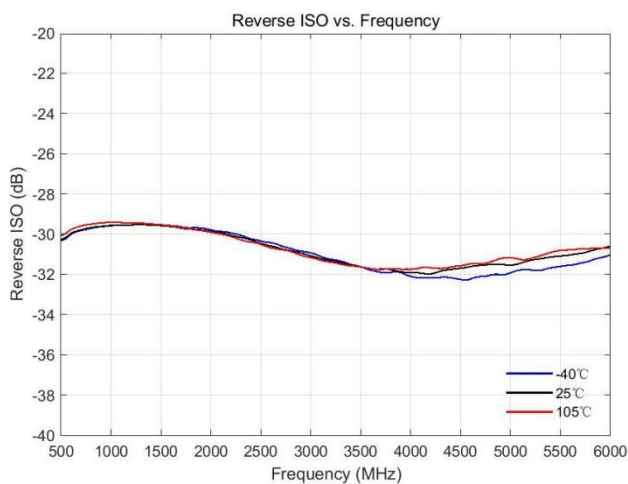


Figure13.

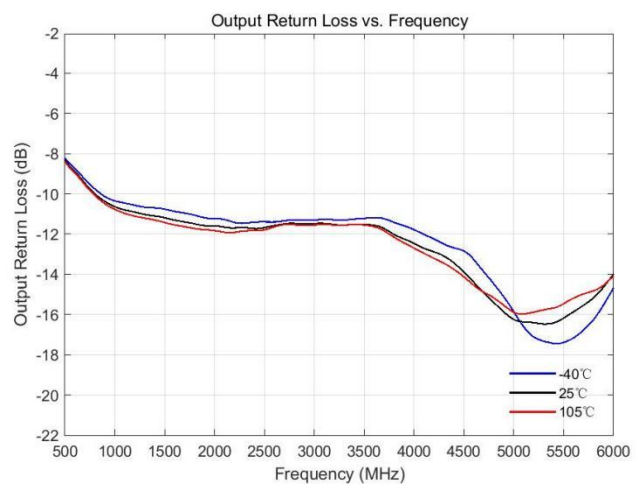


Figure14.

VDD=3.3V Performance Plots (Cont.)

Test Conditions: 50Ω system, VDD=3.3V.

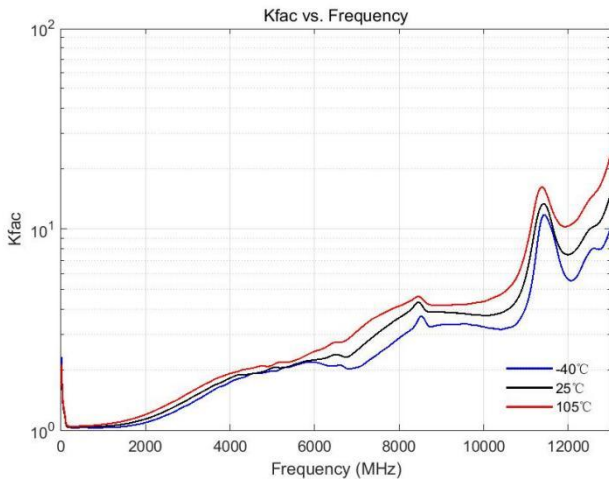


Figure15.

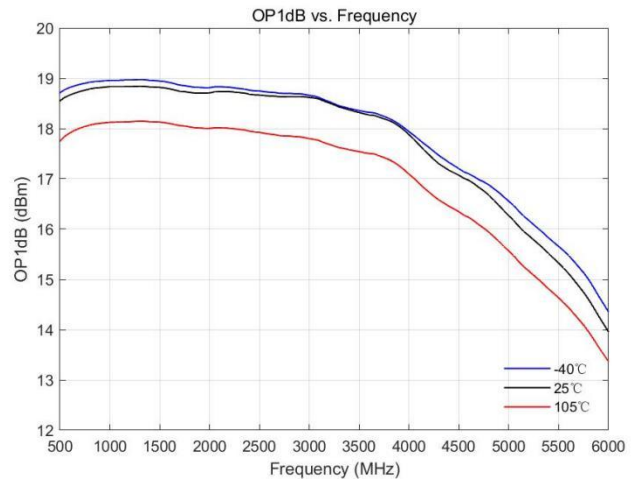


Figure16.

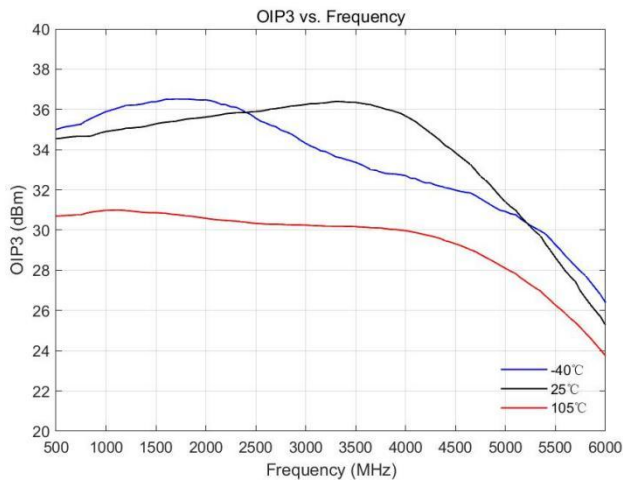


Figure17.

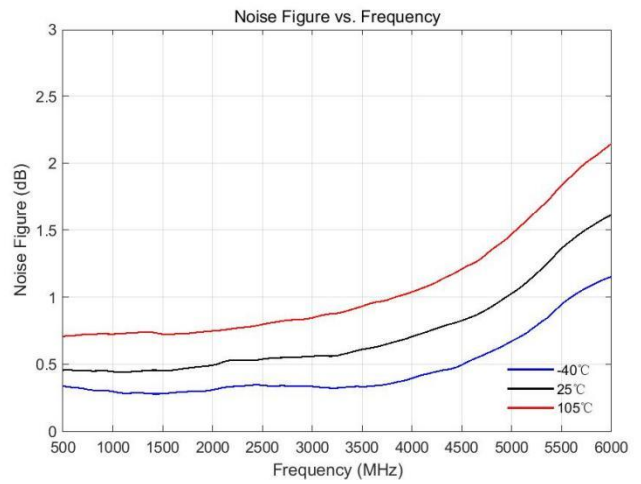


Figure18.

13. Application

13.1. PCB Evaluation Board

The ARF2001 device is typically placed in a system like the one shown below Figure19.

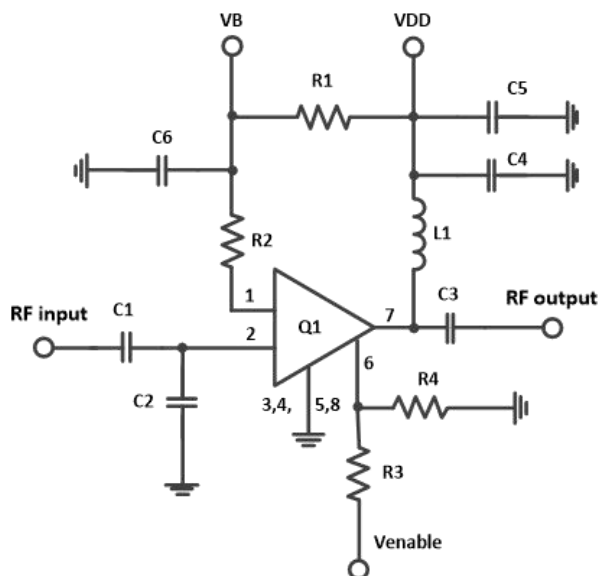


Figure19.

13.2. Evaluation Board BOM

Table8. Bill of Materials for Evaluation PCB

Item	Conditions	Value	Manuf.	Part Num.
PCB	N/A	N/A	ARF	ARF2001-EVB
Q1	N/A	N/A	ARF	ARF2001
R1	N/A	0 Ω	Various	0402
R2	VDD=5V	2k Ω	Various	0402
	VDD=3.3V	430 Ω	Various	0402
R3	N/A	0 Ω	Various	0402
R4	N/A	N/A	N/A	N/A
L1	N/A	30nH	muRata	LQW15
C1	VDD=5V	330pF	Various	0402
	VDD=3.3V	30pF	Various	0402
C2	N/A	N/A	N/A	N/A
C3	N/A	330pF	Various	0402
C4	N/A	100pF	Various	0402
C5	N/A	1uF	Various	0402
C6	N/A	100nF	Various	0402

14. Package Marking and Outline Dimensions

- 1) All dimensions are in millimeters.
- 2) DFN 8 Pin 2x2x0.85mm Package.
- 3) Marking: Part number - 2001
Lot code - XXXX
- 4) Coplanarity applies to the exposed heat sink slug as well as the terminals.
- 5) The package thermal pad must be soldered to the printed circuit board for thermal and mechanical performance.

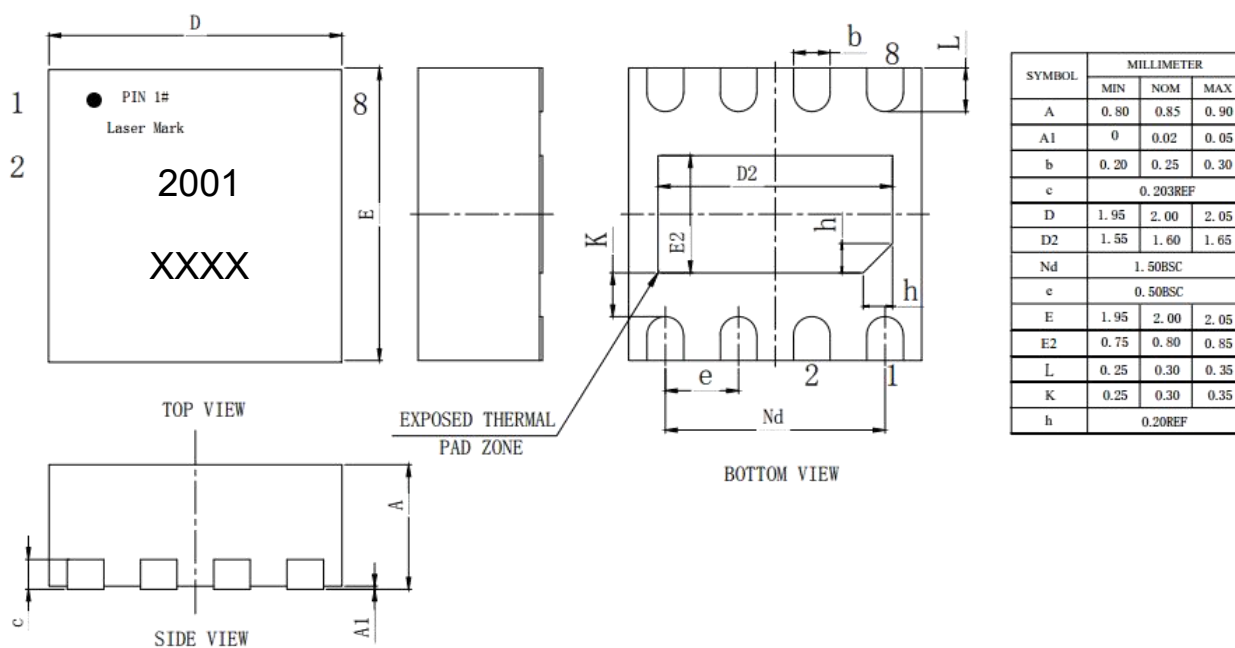


Figure20. Package Marking and Outline Dimensions

15. PCB Mounting Pattern

- 1) All dimensions are in millimeters.
- 2) Vias are required under the backside paddle of this device for proper RF/DC grounding and thermal dissipation.

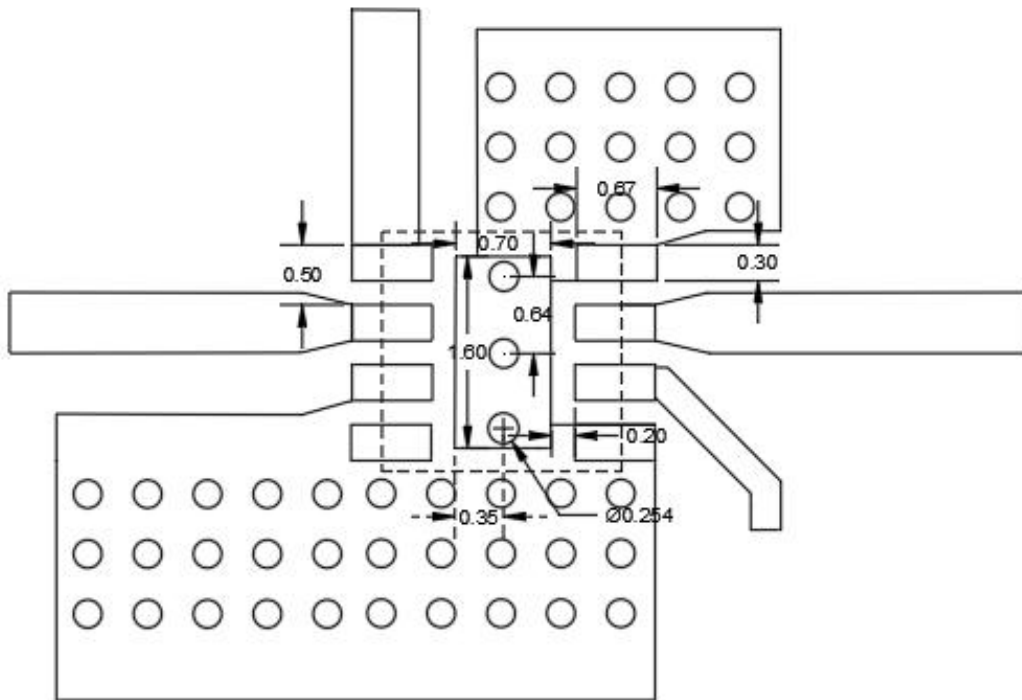


Figure21.PCB Layout Footprint (Top View)

16. Tape and Reel Information

16.1. Carrier and Cover Tape Dimensions

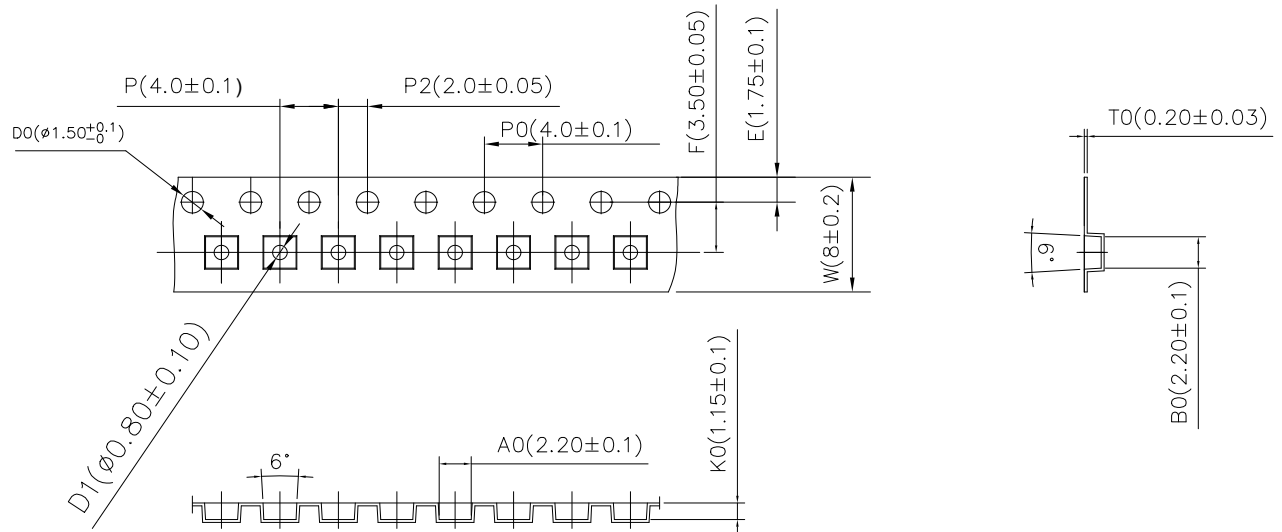


Figure 22. Carrier and Cover Tape Dimensions

16.2. Reel Dimensions

Standard T/R size=3,000 pieces on a 7" reel.

17. Notice

17.1. Operating protection condition



Devices and circuit boards may be undetected. Although this product has an ESD protection circuit, the device may be damaged when encountering high energy ESD. Therefore, appropriate ESD prevention measures should be taken to avoid deterioration of device performance or loss of function.

17.2. Operate attention

1. Must be placed in a container with electrostatic protection function, dry environment, conditions permit the best storage nitrogen environment.
2. Please strictly comply with the ESD protection requirements to avoid electrostatic damage.
3. Use vacuum clamps or tweezers to avoid tools or fingers touching the product surface.

17.3. Solderability

Compatible with lead-free (260 °C maximum reflow temperature) soldering processes.

17.4. RoHS Compliance

This product is compliant with the EU RoHs2.0, EU Directive 2015/863.

17.5. Contact Information

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